
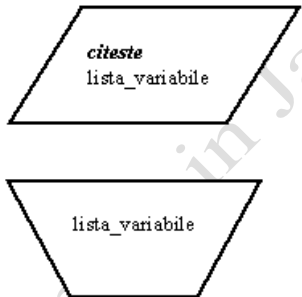
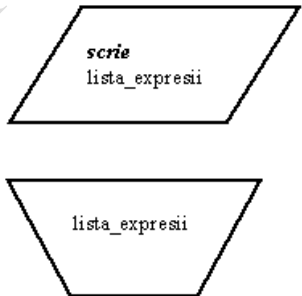
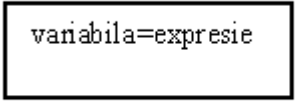
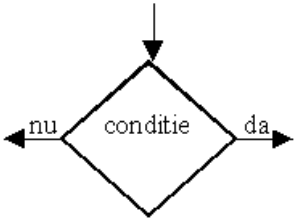


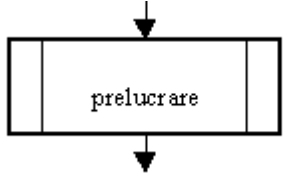
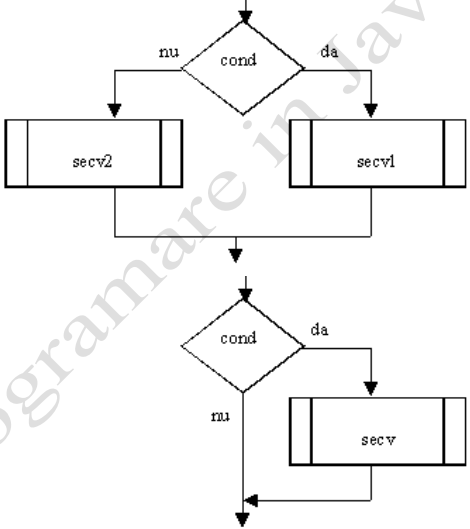
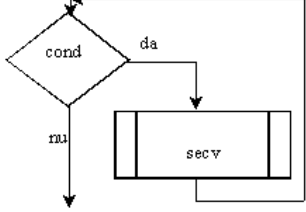
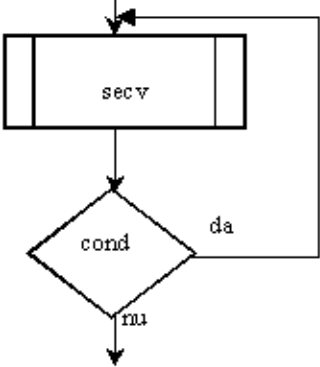
In **schemele logice** se folosesc blocurile indicate in **Tabelul 2.1**.

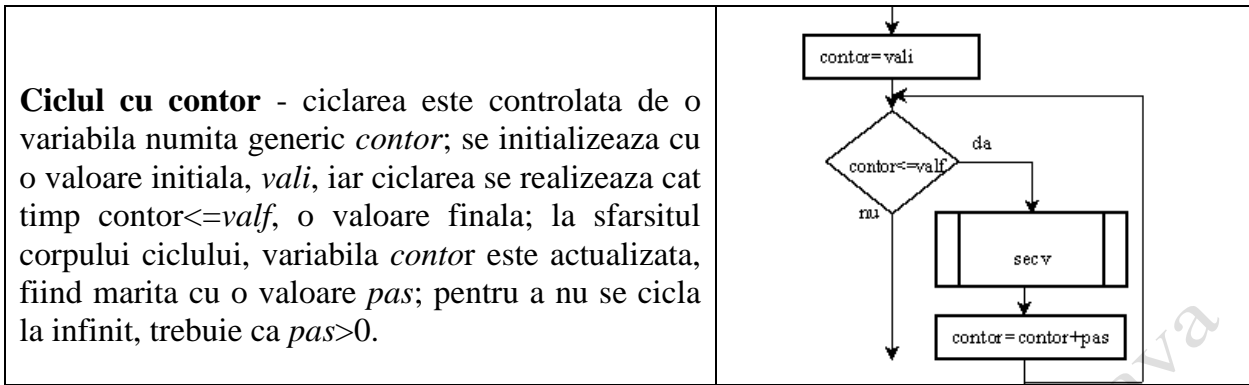
<p>Blocul de inceput/sfarsit - orice schema logica incepe cu un bloc de inceput (start) si se termina cu blocul de sfarsit (stop)</p>	
<p>Blocul de citire (doua variante) - se citesc de la dispozitivul de intrare valorile variabilelor specificate in lista_variabile (separate prin virgula); se folosește la introducerea datelor (citire)</p>	
<p>Blocul de scriere (doua variante) - se scriu la dispozitivul de iesire valorile obtinute in urma evaluarii expresiilor din lista (separate prin virgula); se folosește la afișarea rezultatelor (scriere)</p>	
<p>Blocul de atribuire - se evalueaza expresia, iar valoarea obtinuta este memorata in variabila, vechea valoare pierzandu-se; expresia contine operatori (+ - * /), operanzi (variabile, constante) si (); variabila poate apare si in expresie (ex: a=a+5)</p>	
<p>Blocul de decizie - se evalueaza conditia: daca este adevarata se continua cu prelucrarea indicata de ramura da, altfel se continua cu prelucrarea indicata de ramura nu; conditia poate contine operatori relationali: < > <= >= == != operatori logici: si, sau etc</p>	

Tabelul 2.1 Blocuri folosite în schemele logice

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Structurile de control de baza impreuna cu cele derivate sunt prezentate in **tabelul 2.2**.

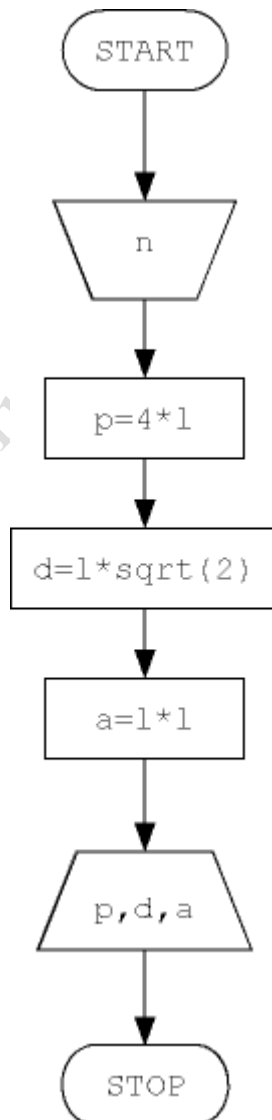
<p>Secventa - indica executia succesiva a operatiilor de baza si a structurilor de control in ordinea in care apar in schema logica; in general, orice schema logica cuprinde secventele: de initializare a variabilelor, prelucrari, tiparirea rezultatelor</p>	
<p>Selectia - functie de valoarea de adevar a conditiei, se executa una din secvente, dupa care se trece la prelucrarea urmatoare; cele doua ramuri se exclud mutual; este posibil ca una din ramuri sa fie vida</p>	
<p>Ciclul cu test initial - cat timp conditia este adevarata, secventa (corpul ciclului) se executa ciclic; daca la prima evaluare a conditiei, aceasta este falsa, corpul nu se executa niciodata.</p>	
<p>Ciclul cu test final - conditia se evalueaza dupa executia secventei (corpul ciclului) care se executa cel putin o data; se revine la executia secventei, daca este adevarata conditia.</p>	



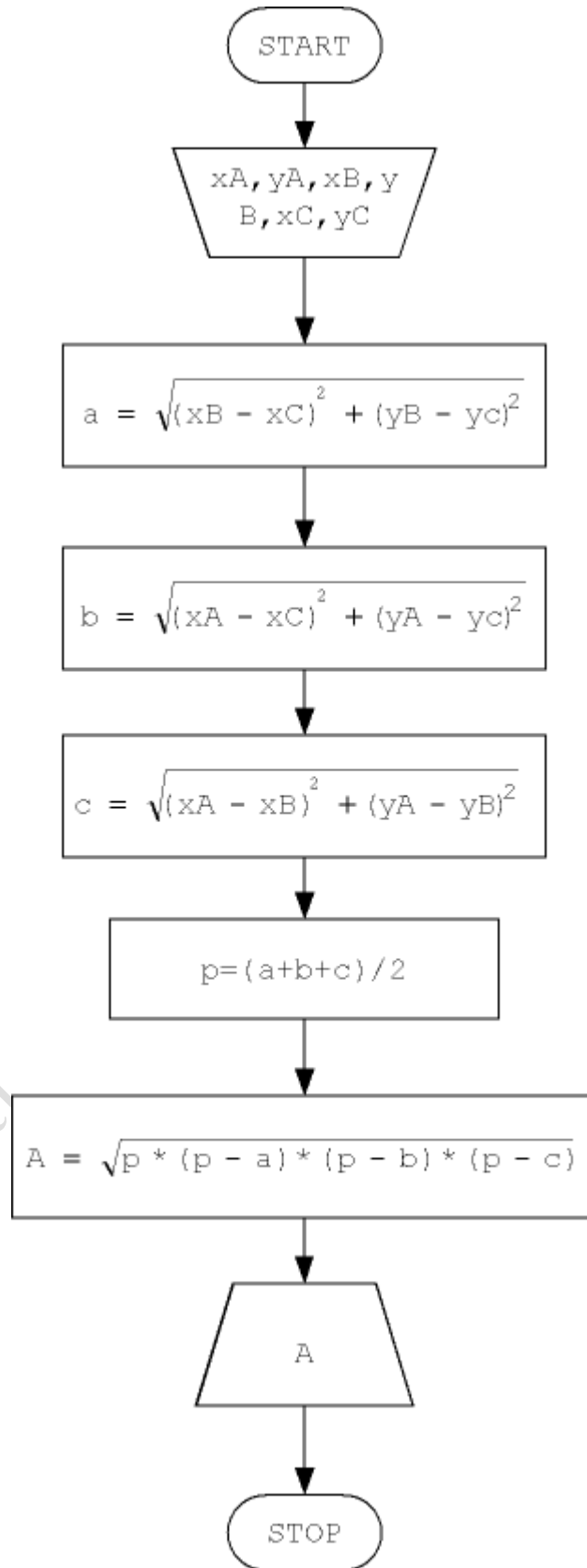
Tabelul 2.2 Structurile de control de baza si derivate

Scheme logice – Solutii pentru aplicatiile A2.1 – A2.20

A2.1



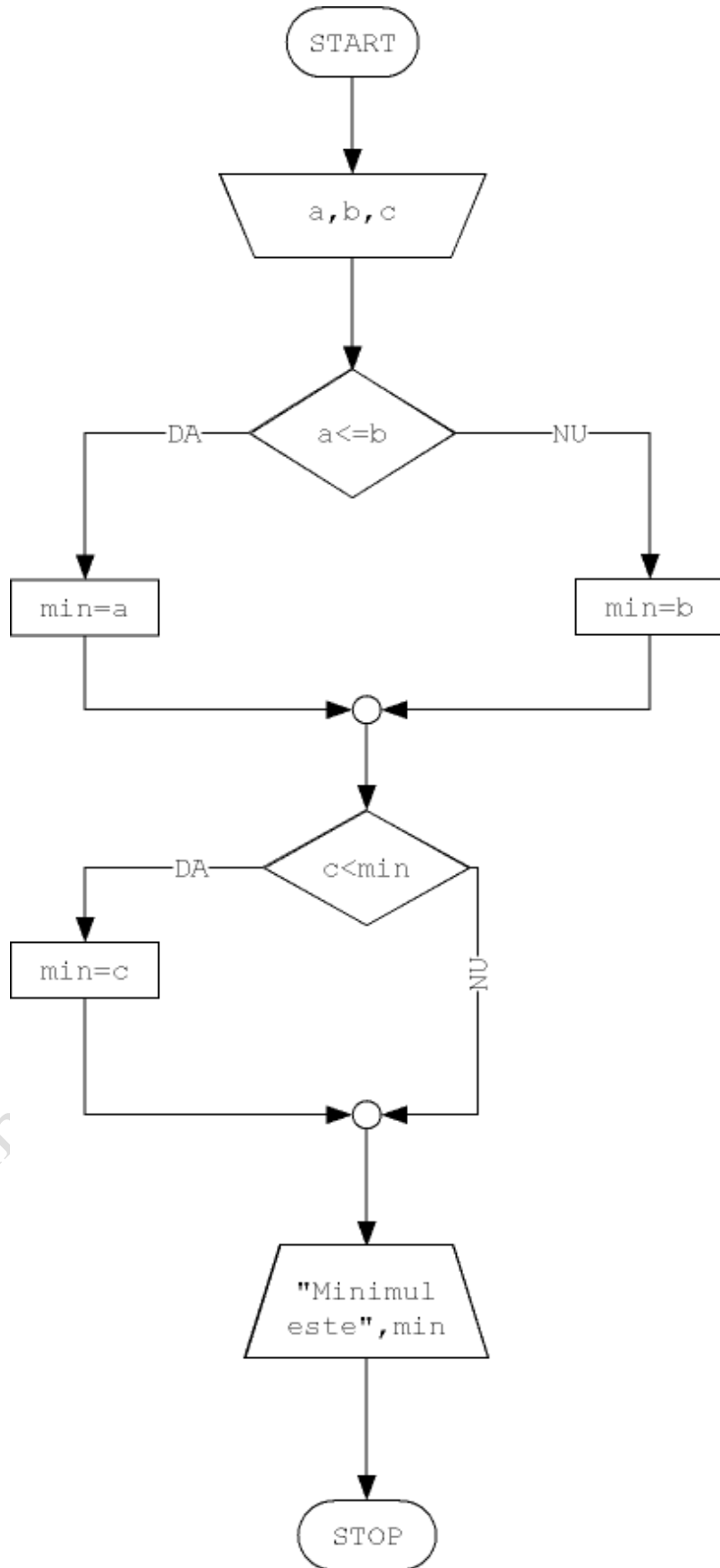
A2.2



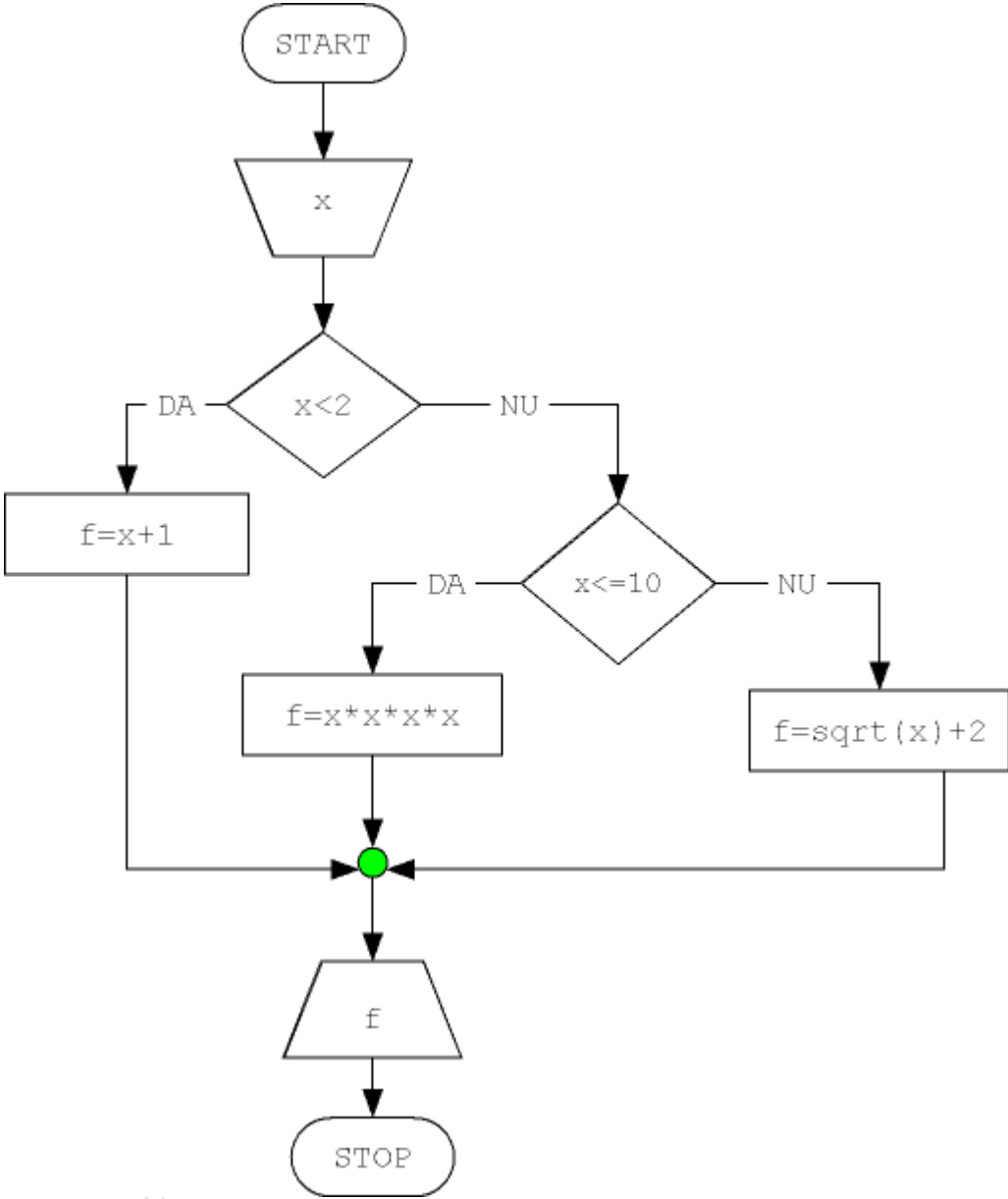
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A2.3



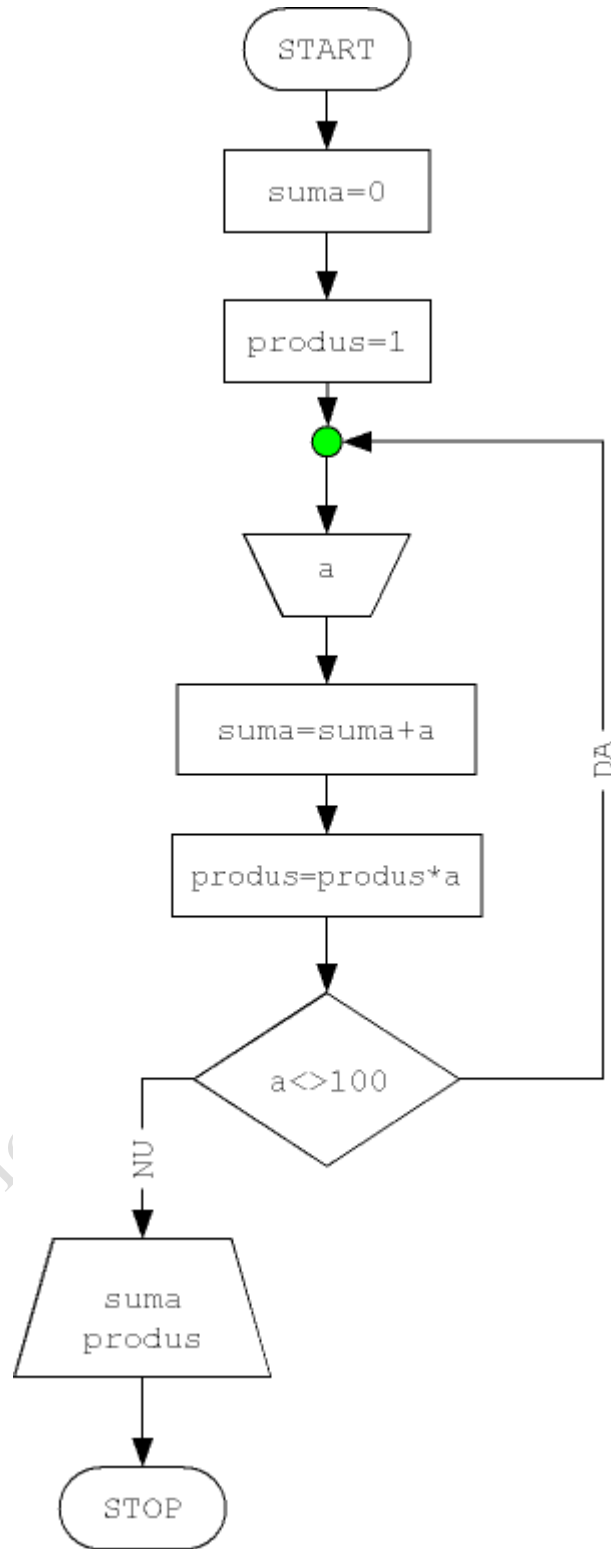
A2.4



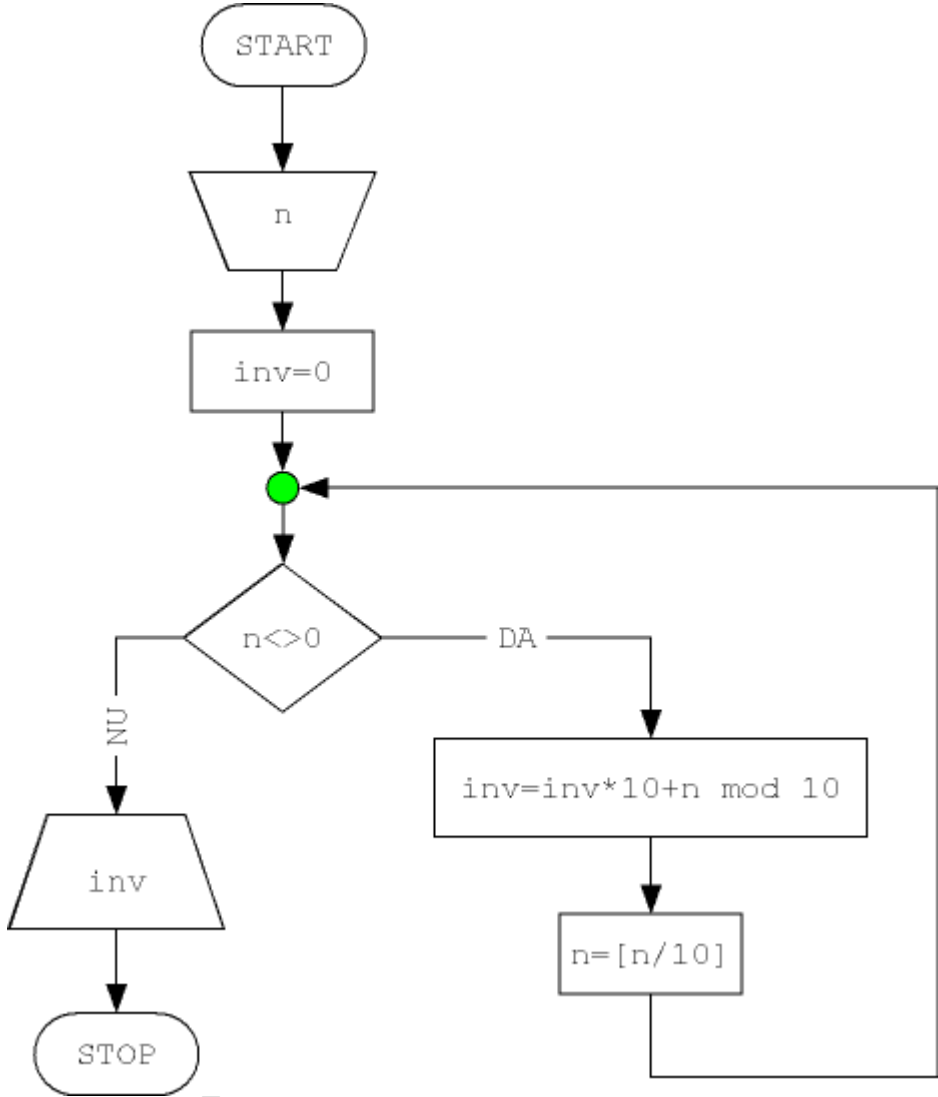
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A2.5



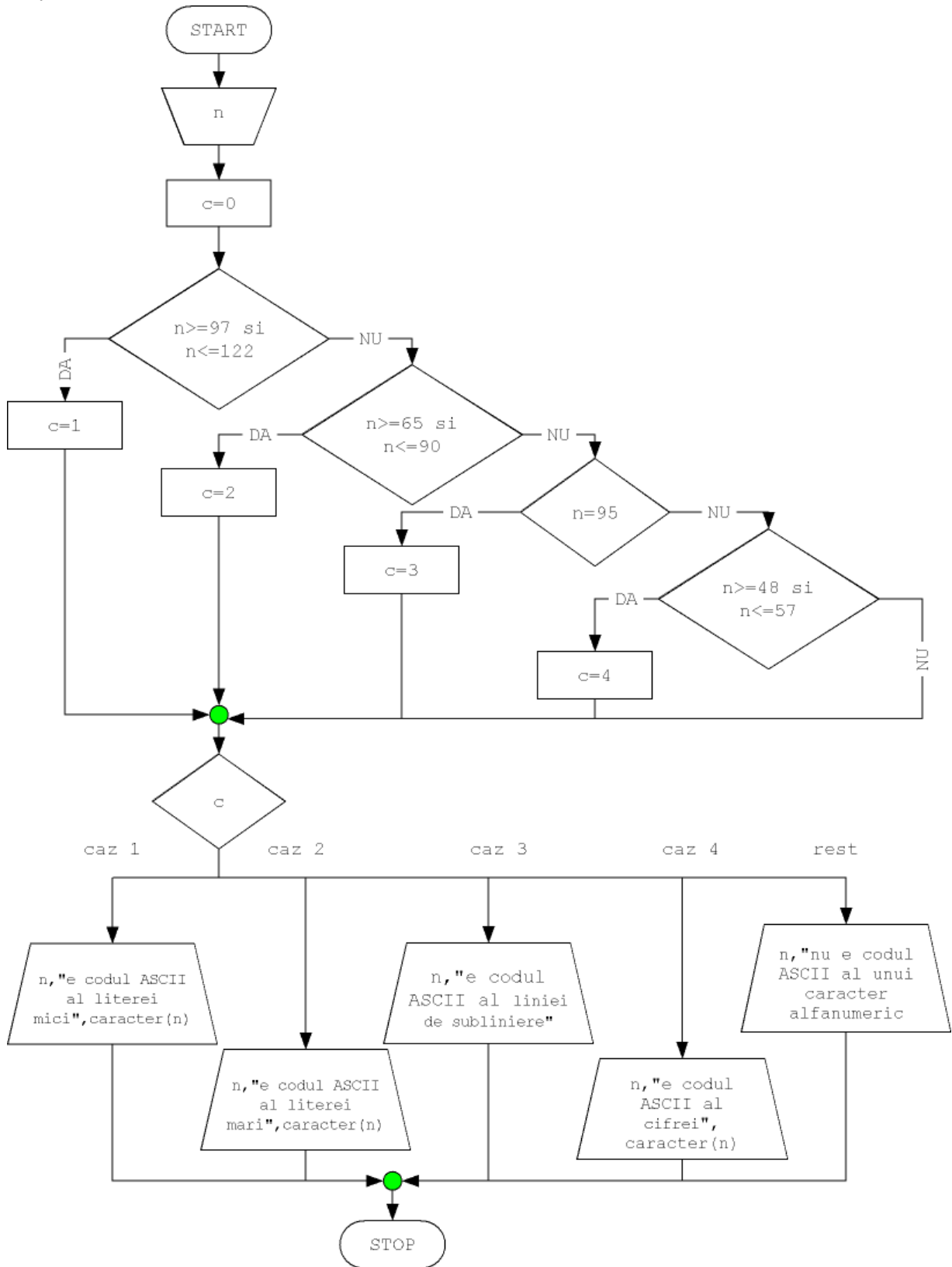
A2.6



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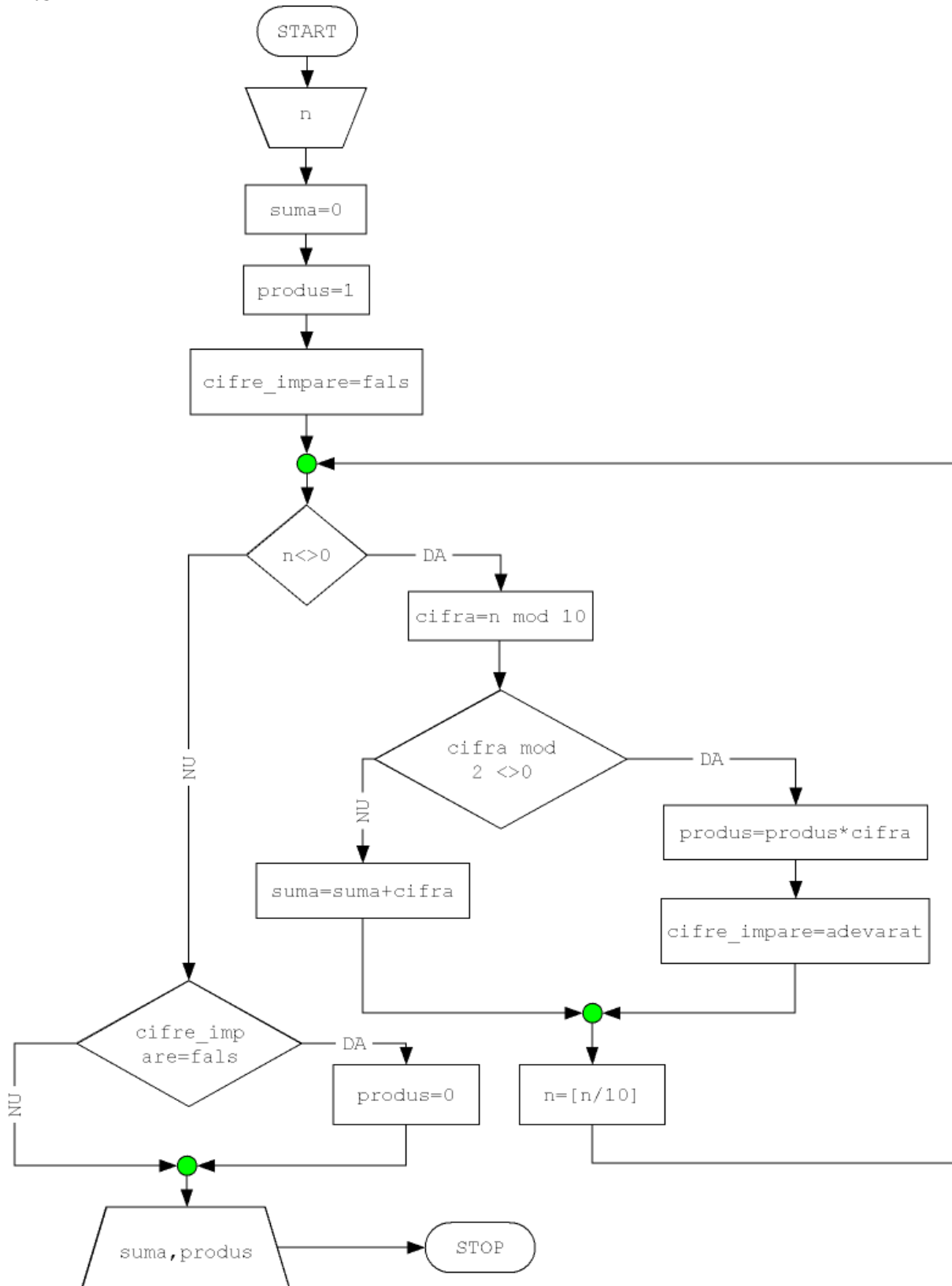
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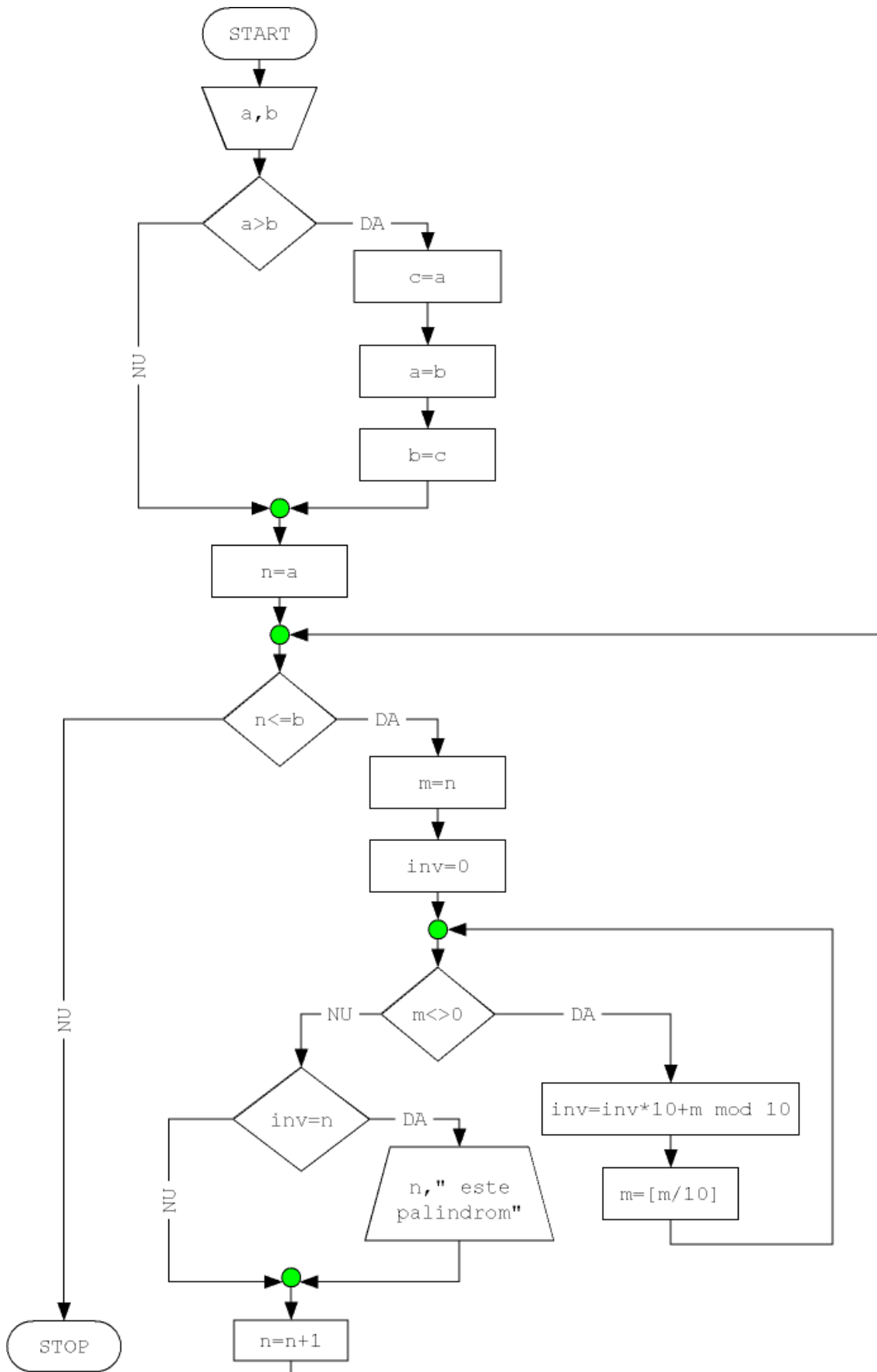
A2.7



Despre date si algoritmi

A2.8

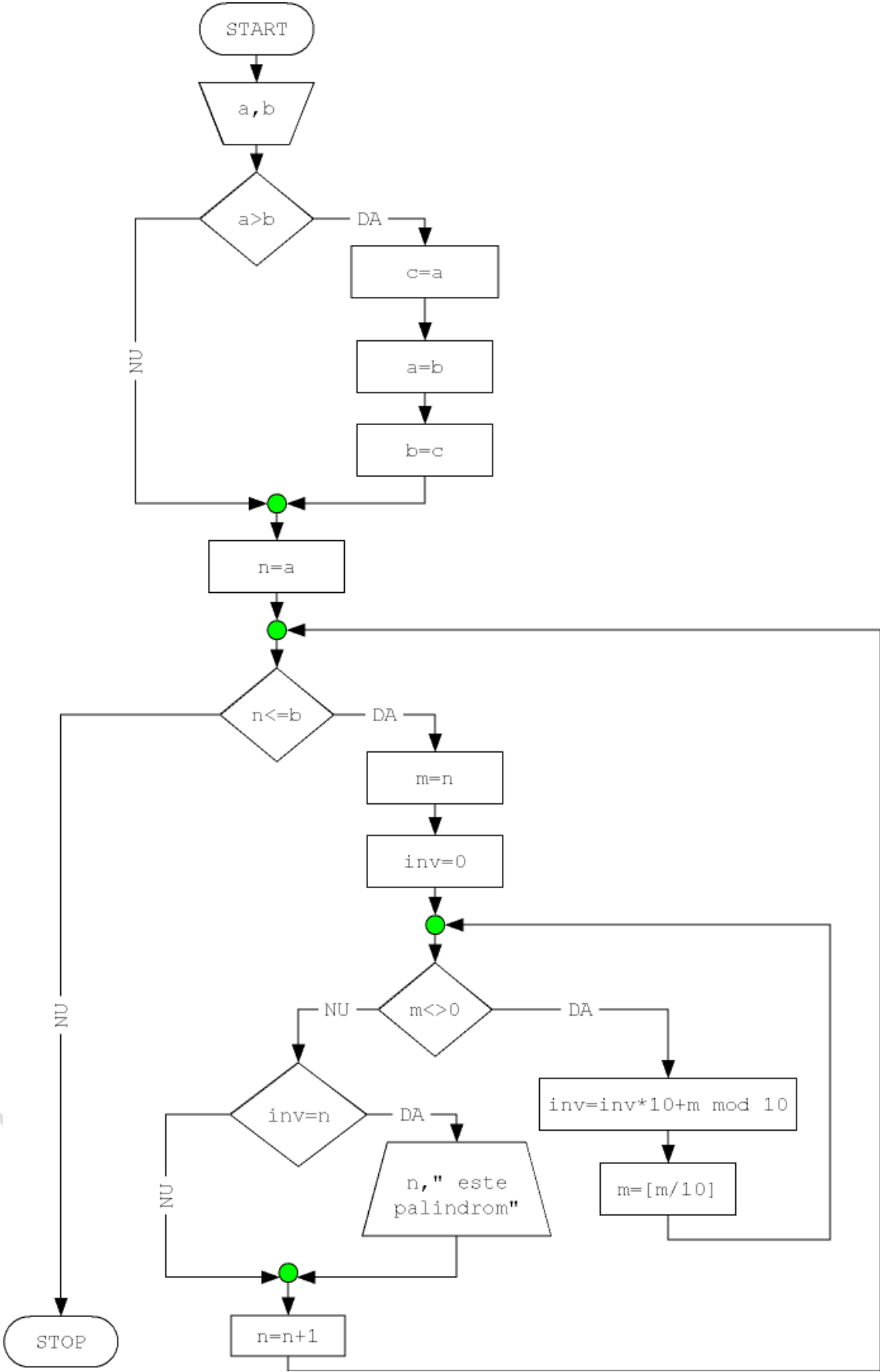




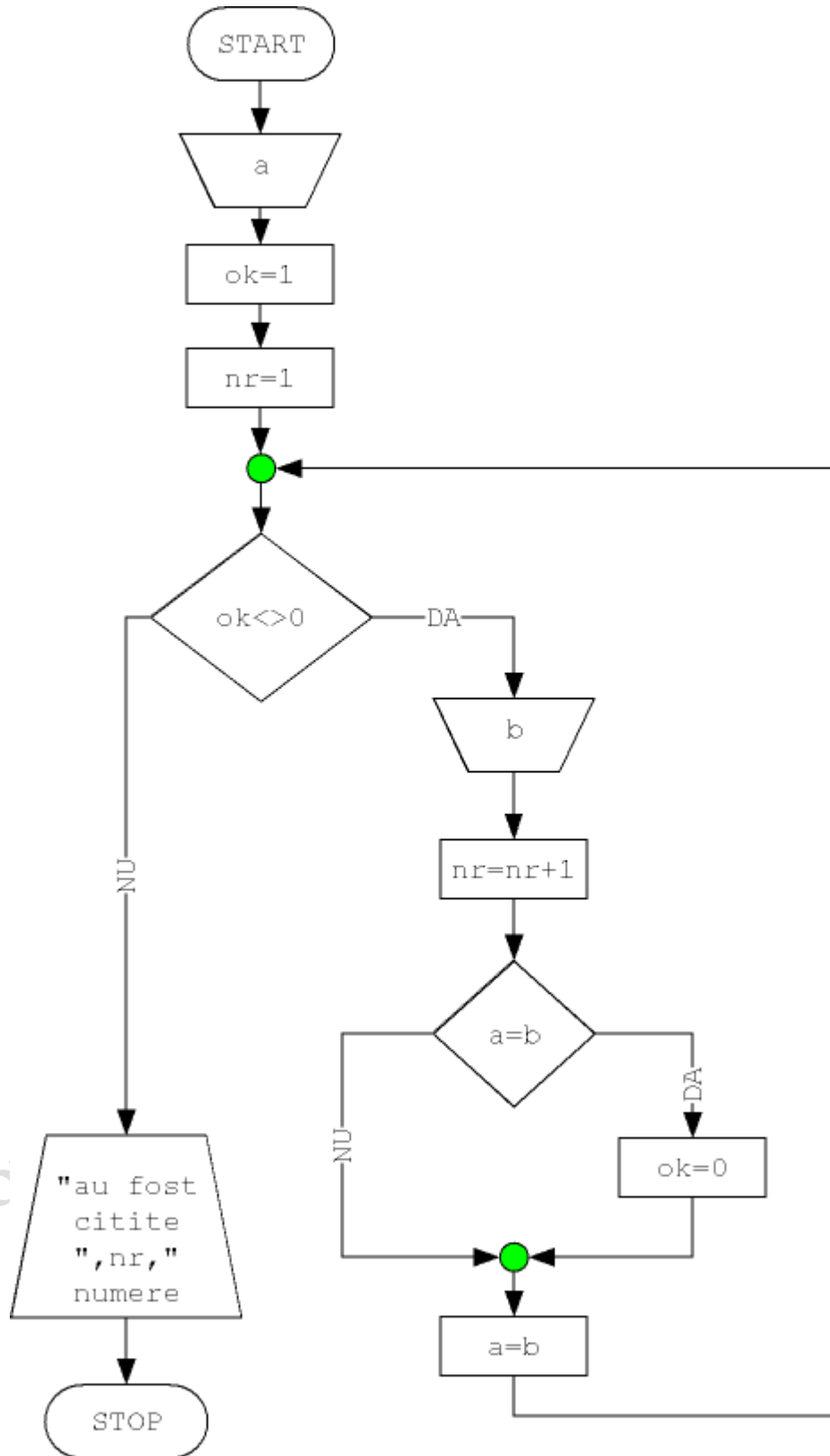
va

Despre date si algoritmi

A2.9

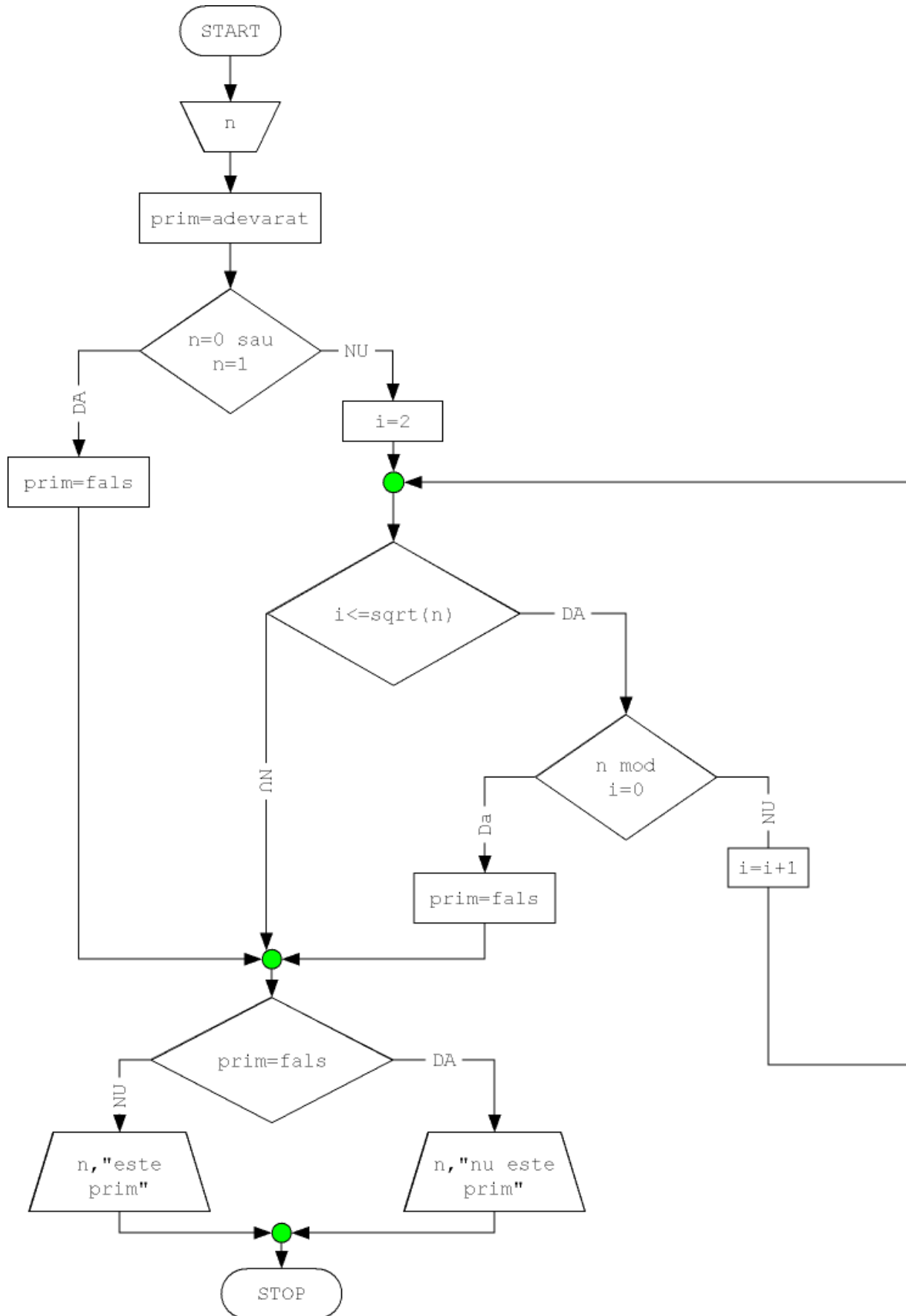


A2.10

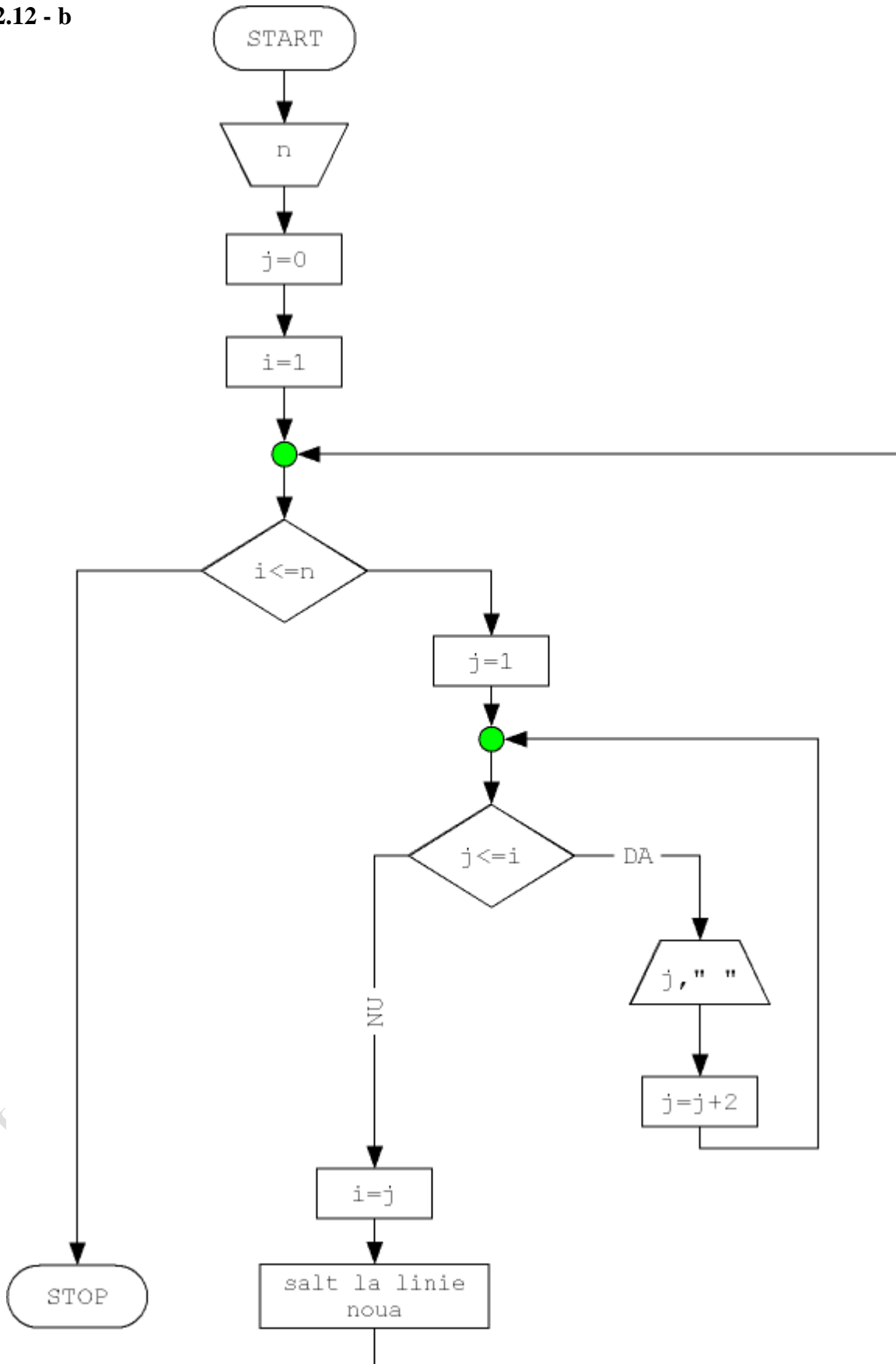


Despre date si algoritmi

A2.11

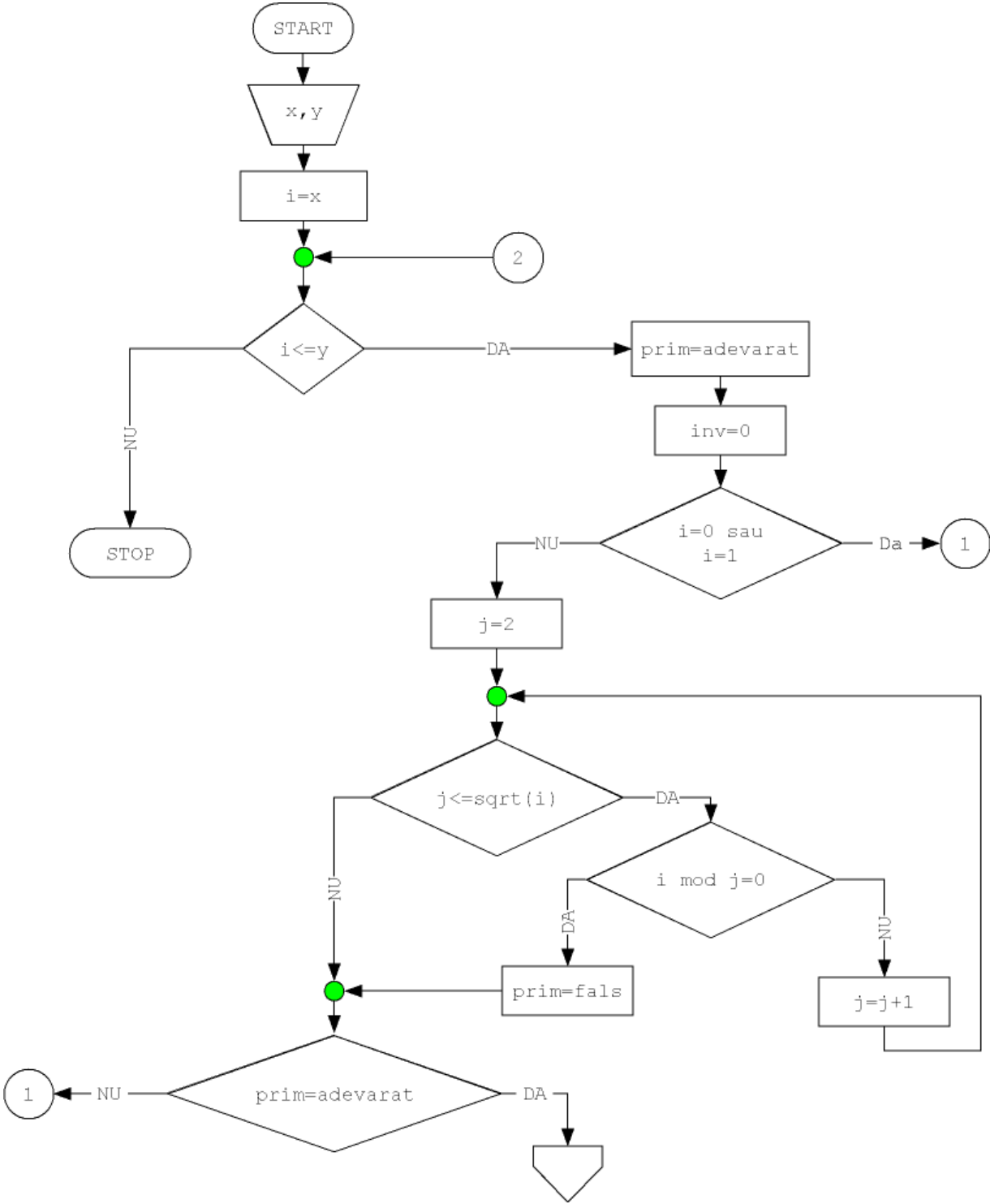


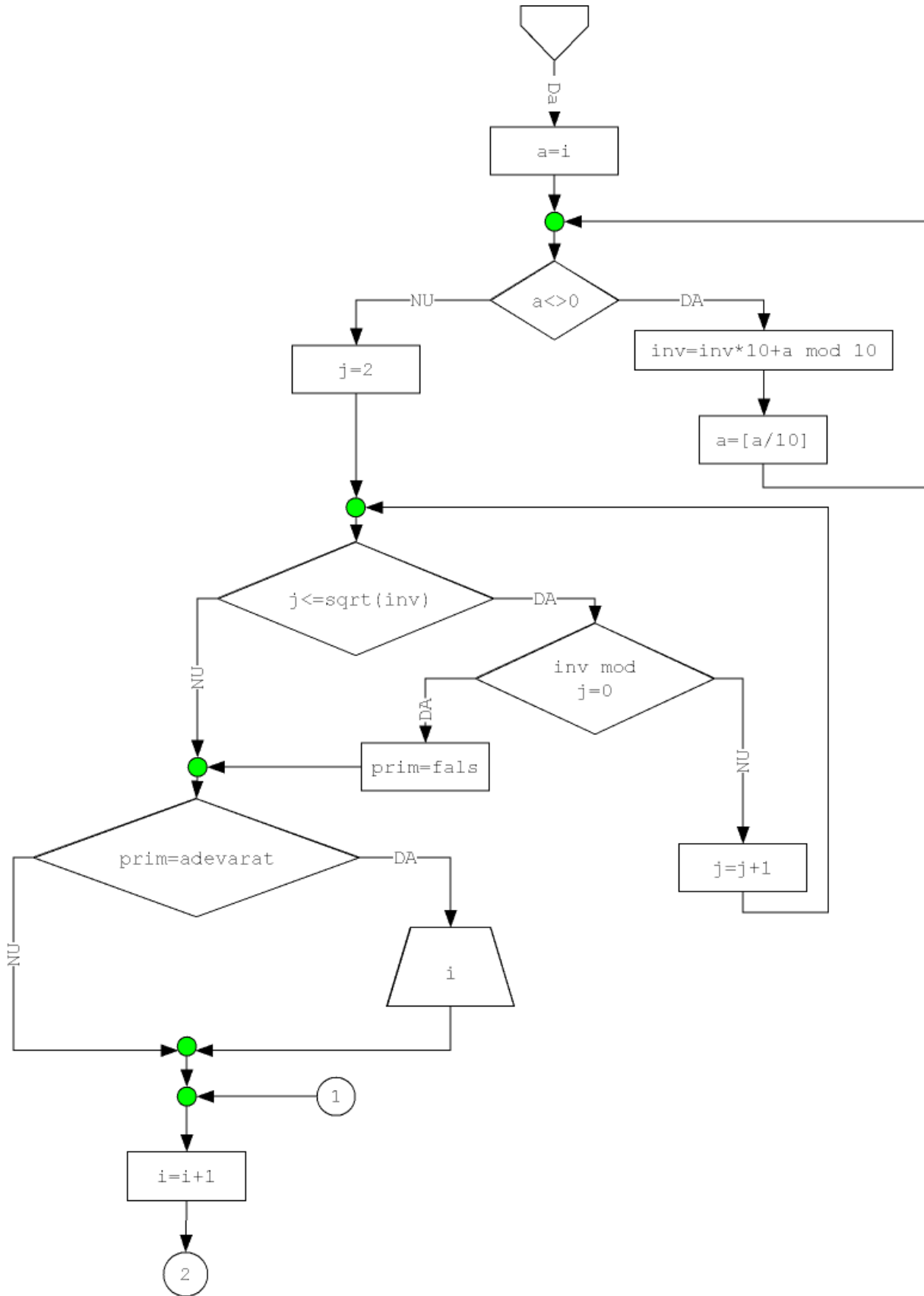
A2.12 - b



Despre date si algoritmi

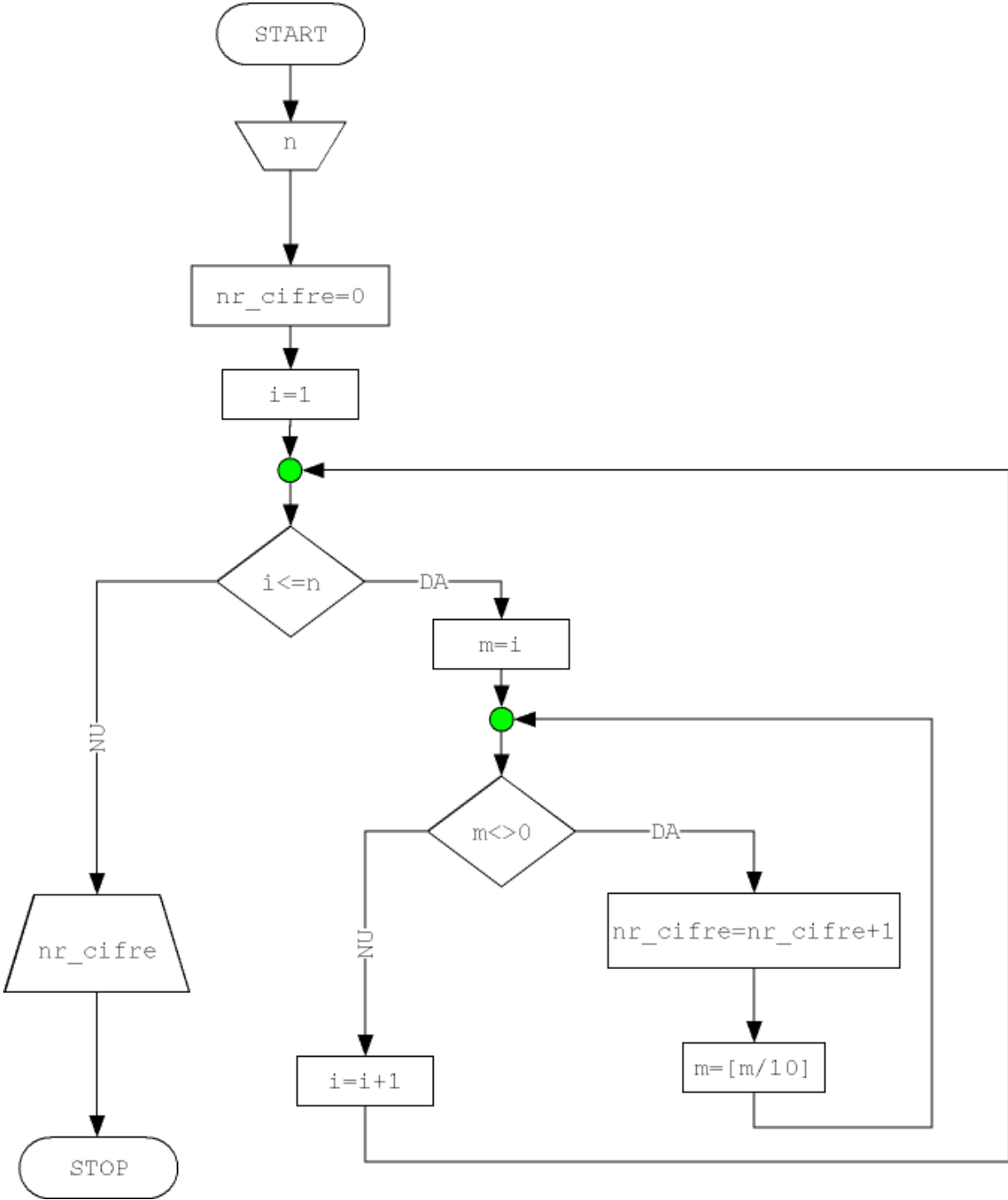
A2.13



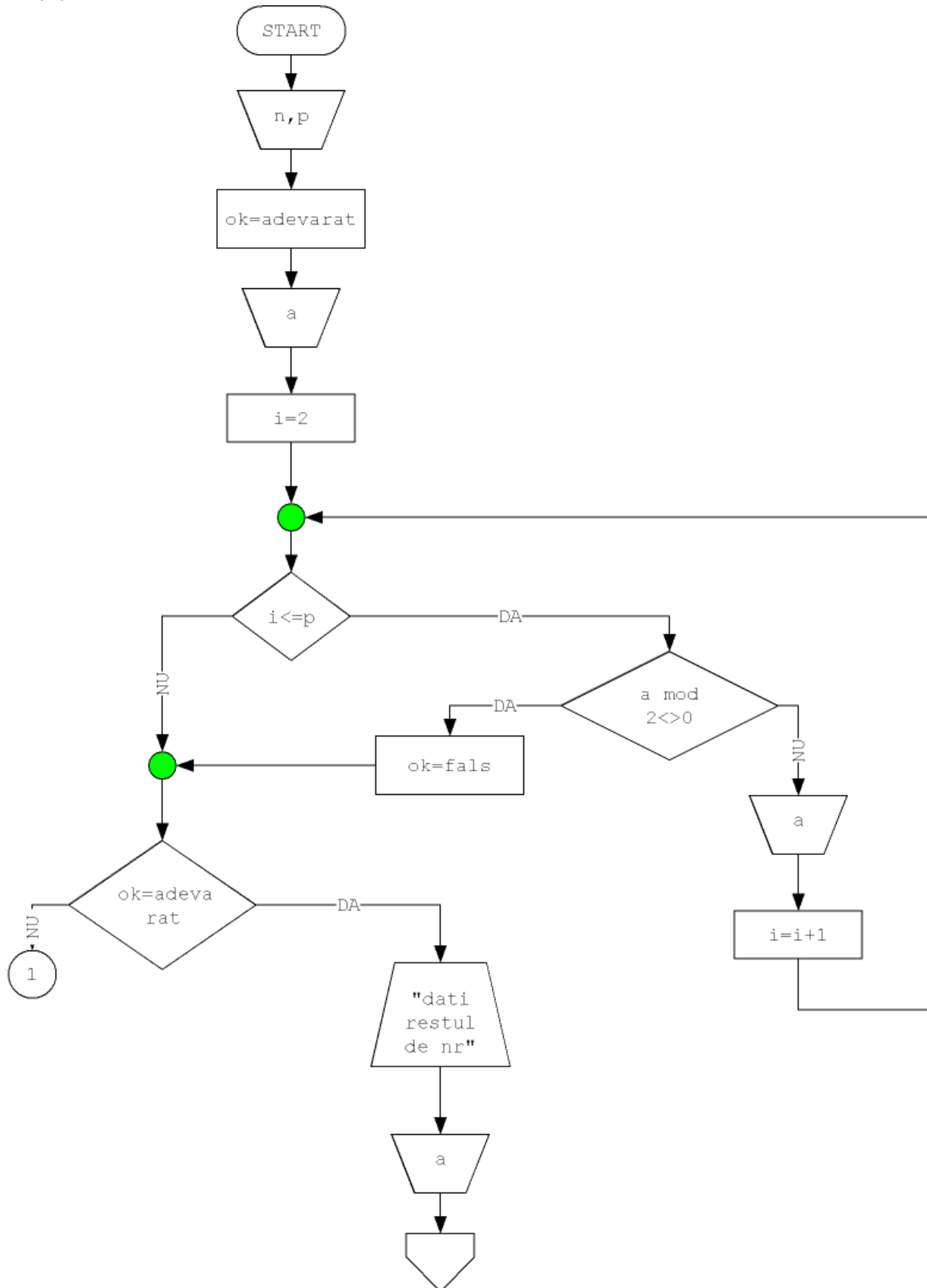


Despre date si algoritmi

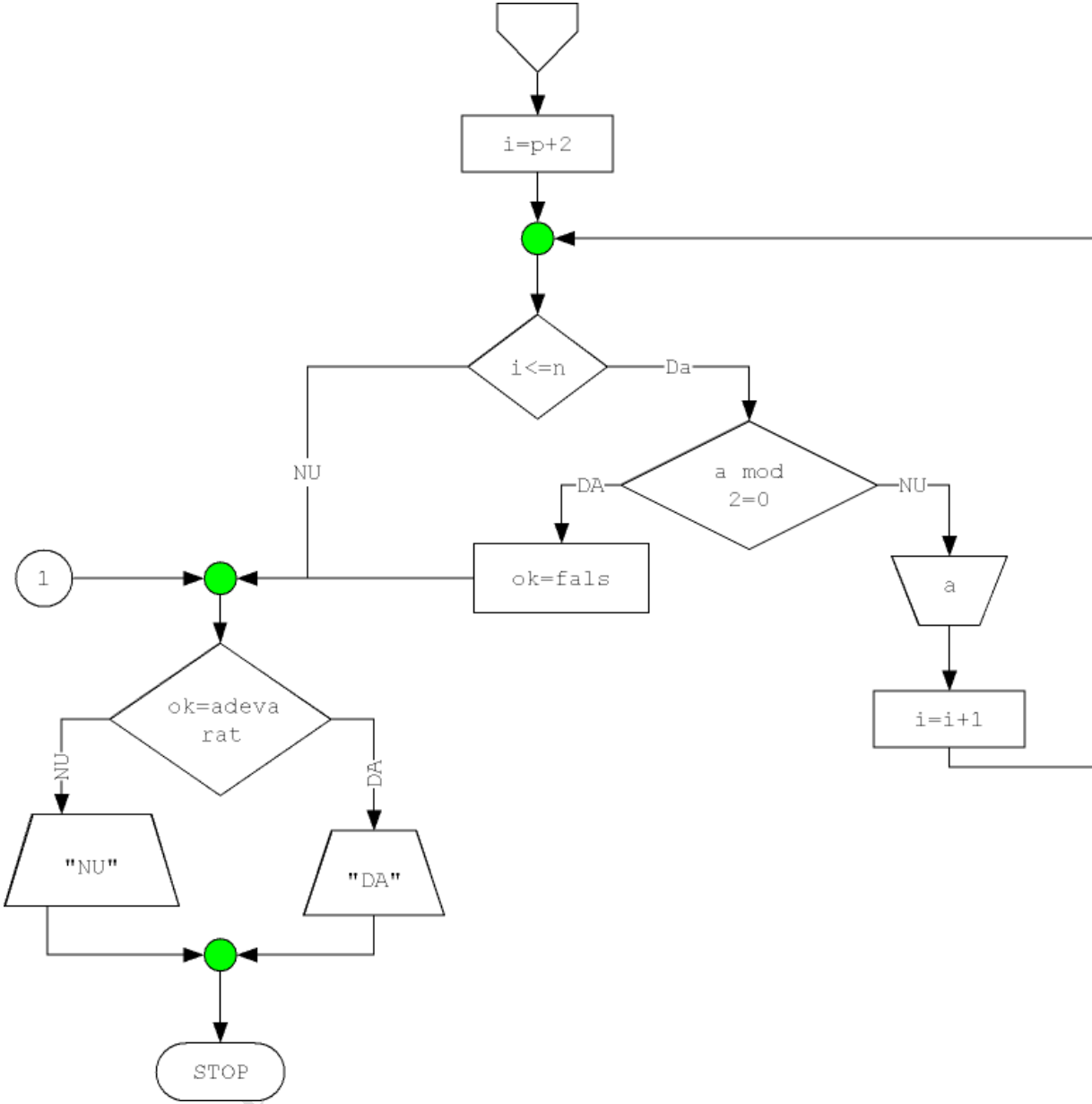
A2.14



A2.15

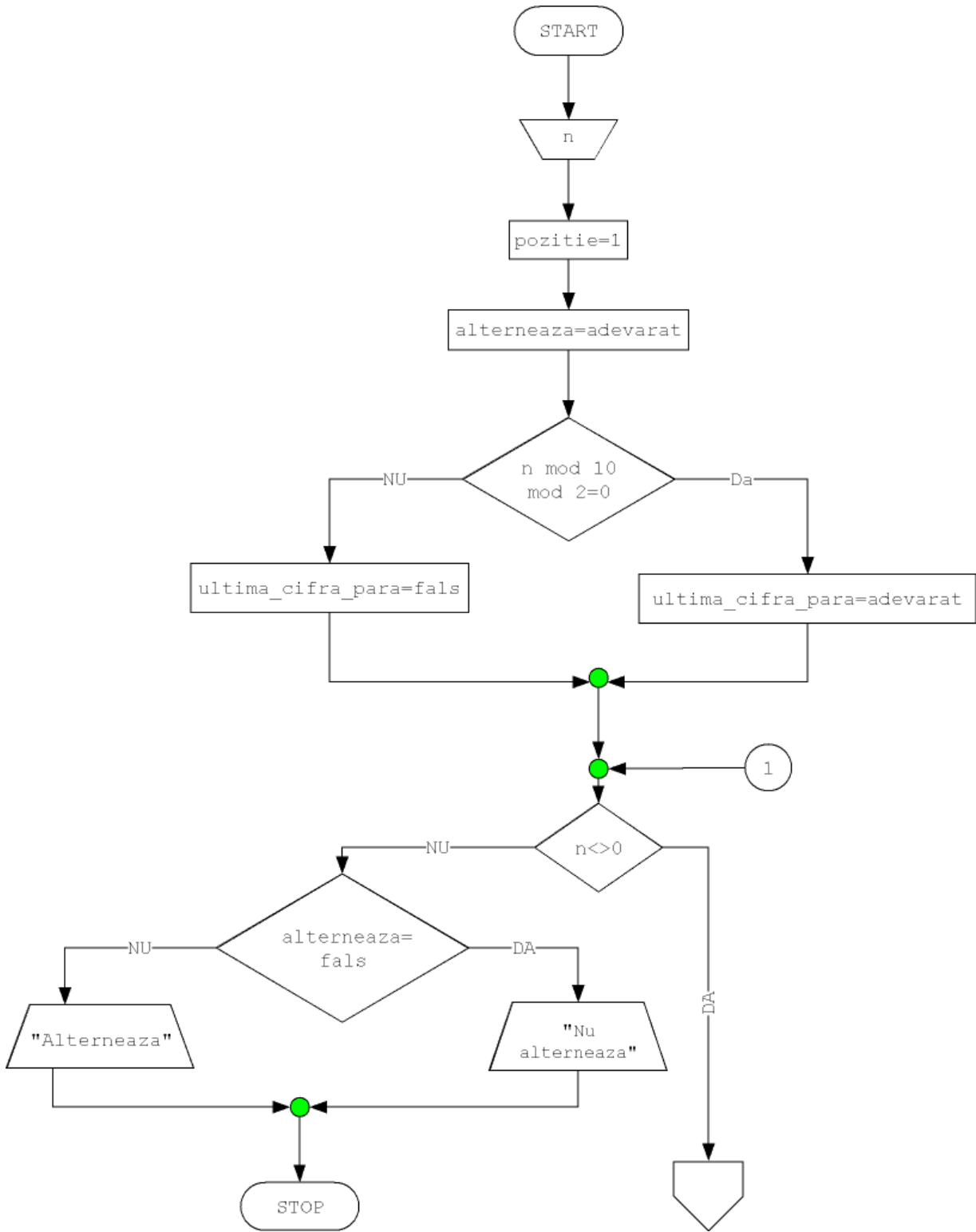


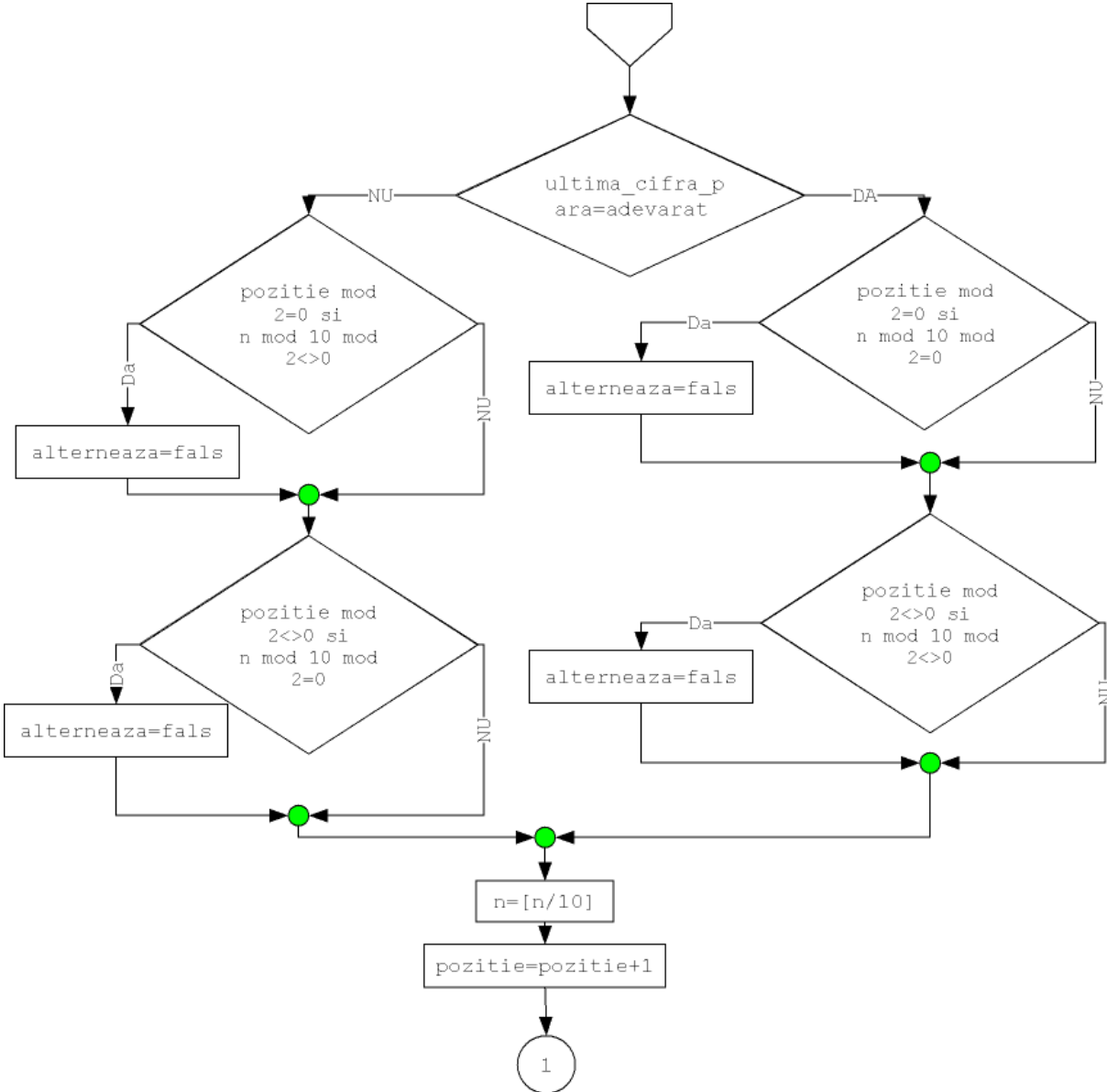
Despre date si algoritmi



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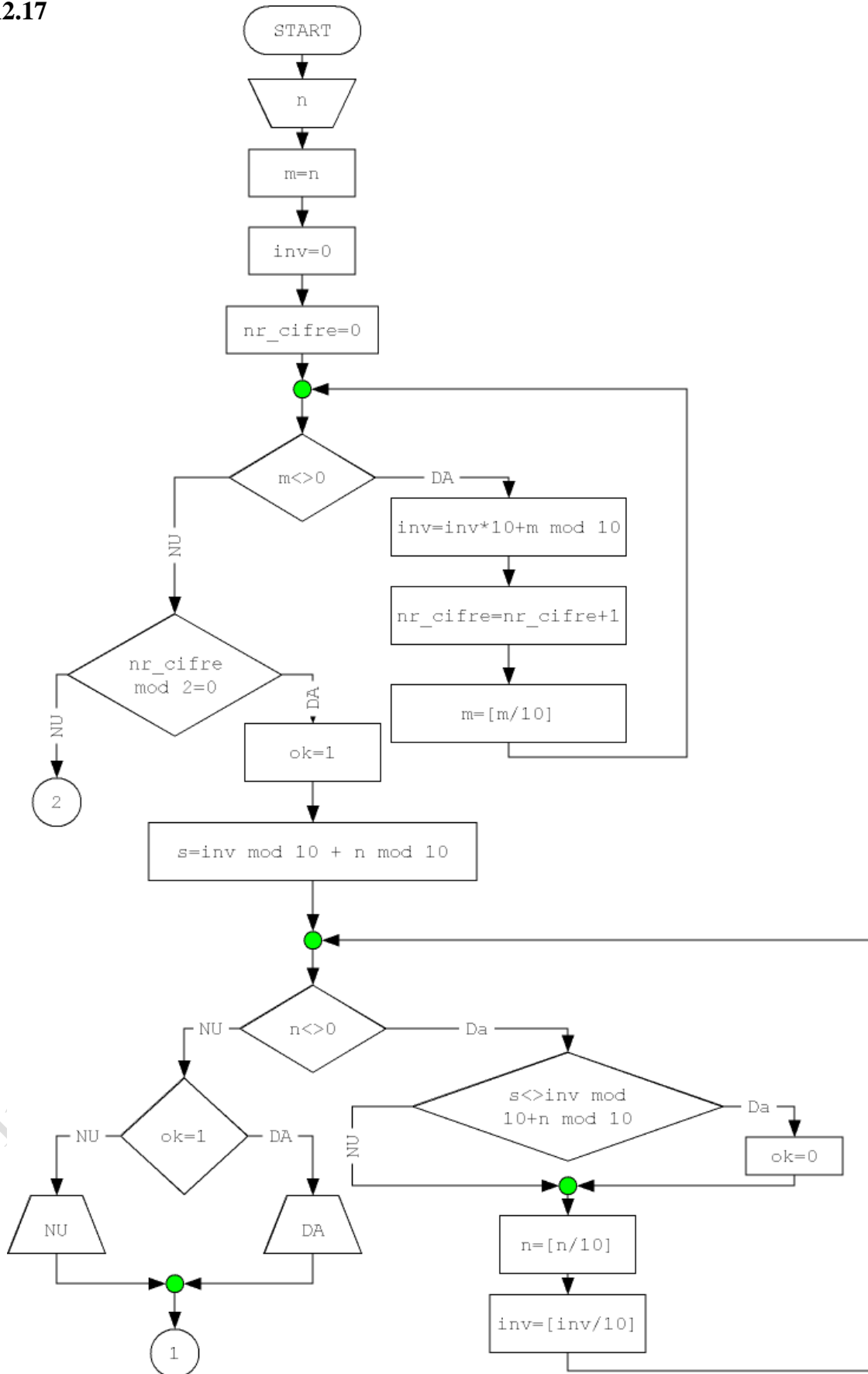
A2.16



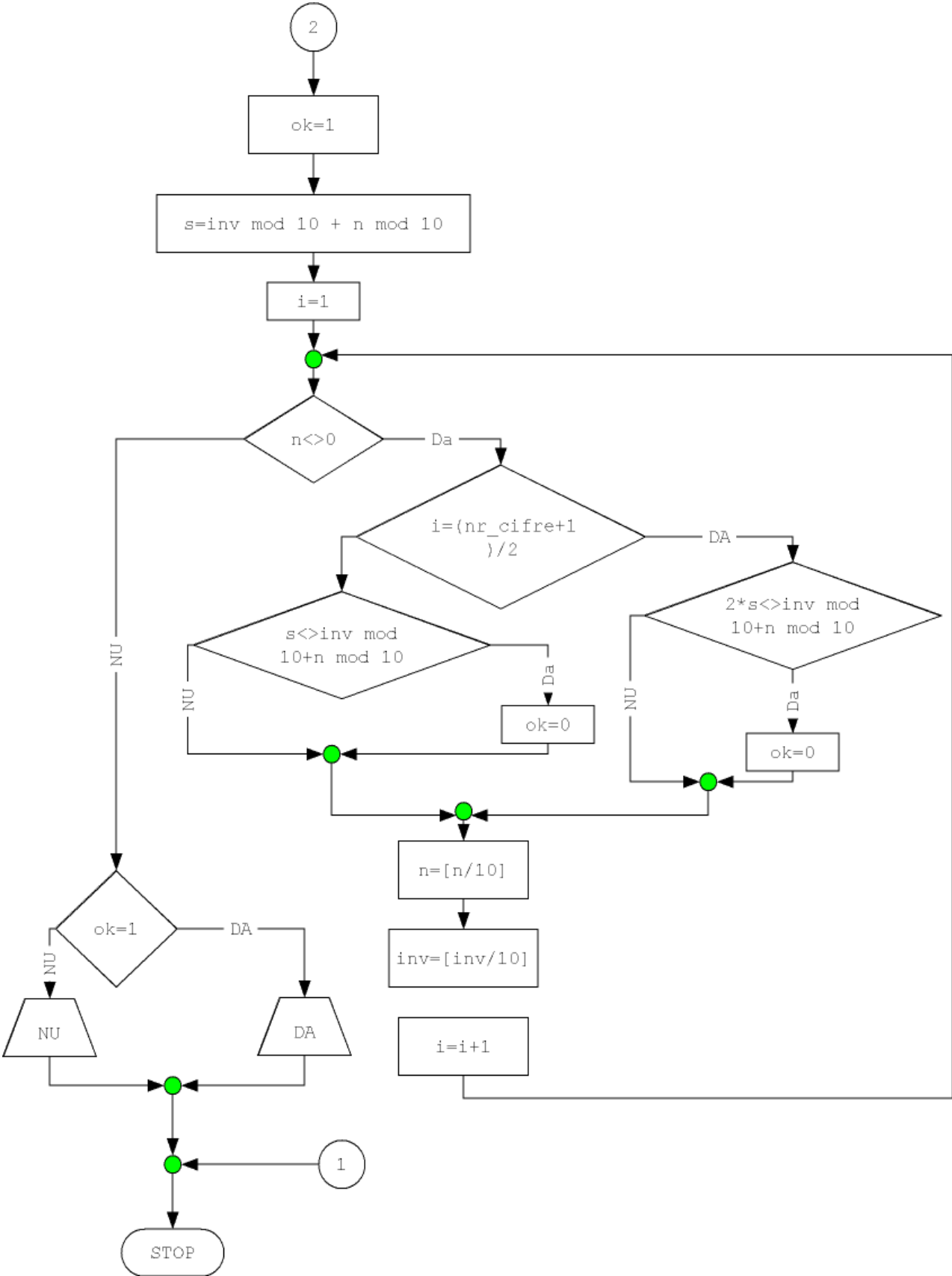


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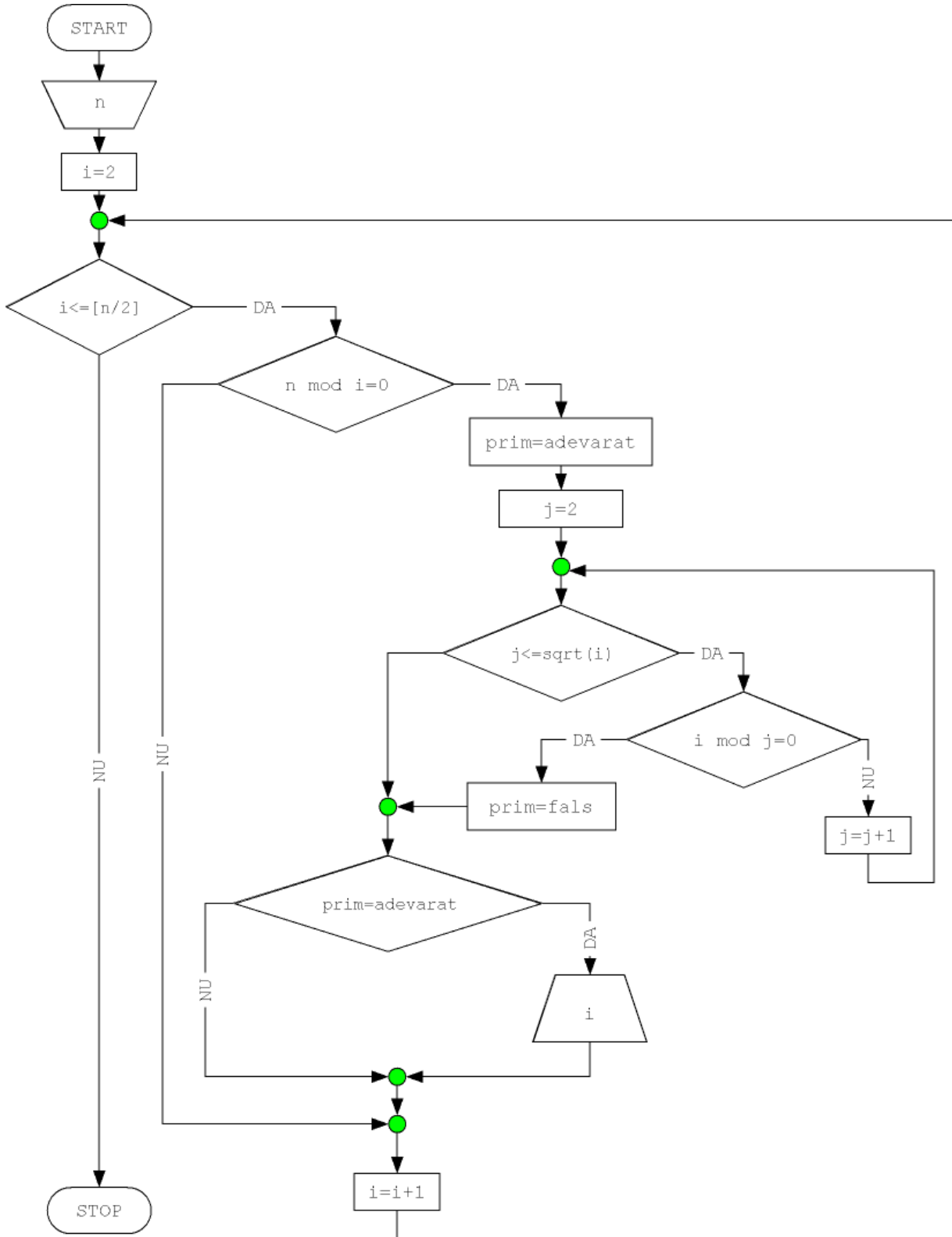
A2.17



Despre date si algoritmi

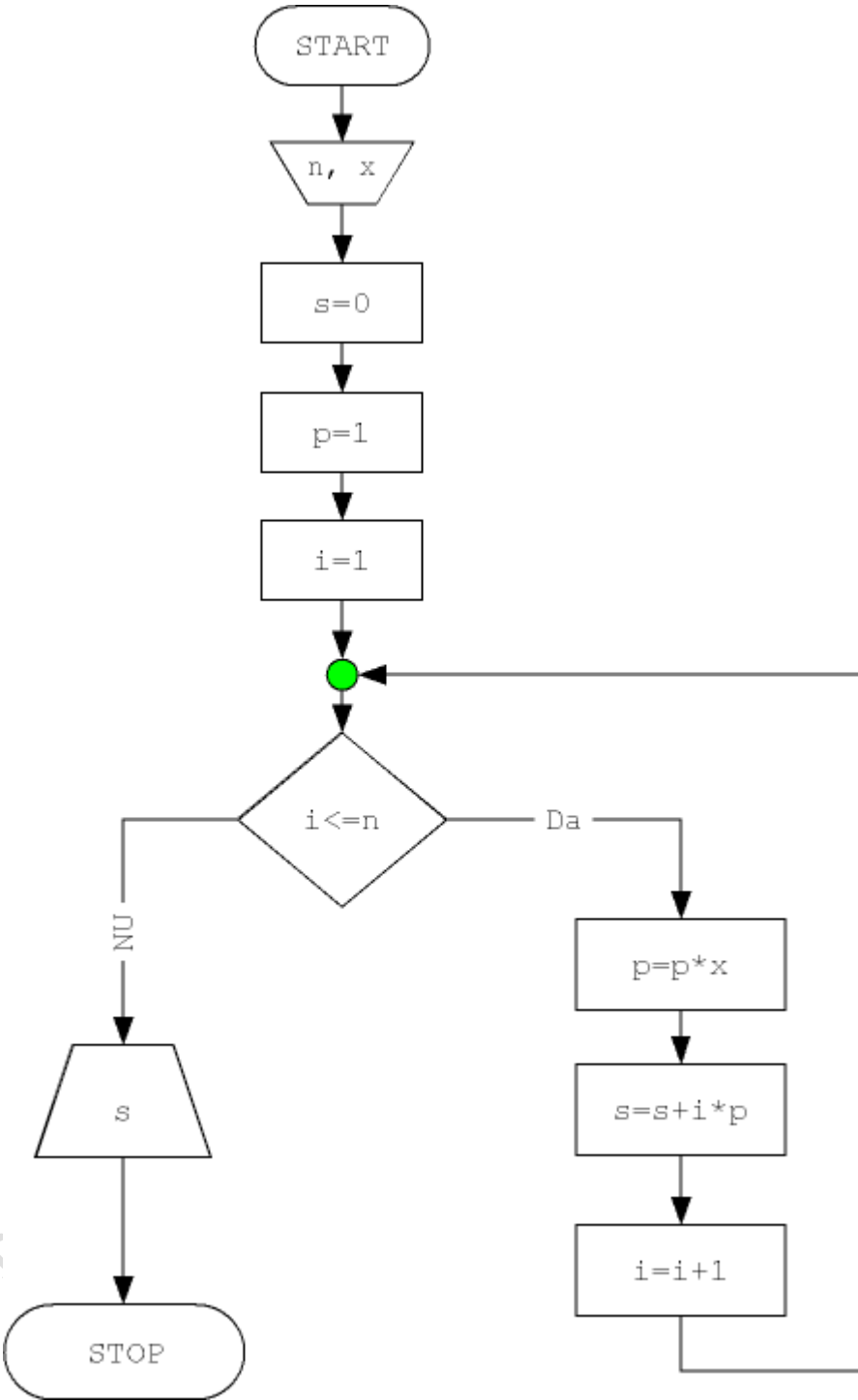


A2.18



Despre date si algoritmi

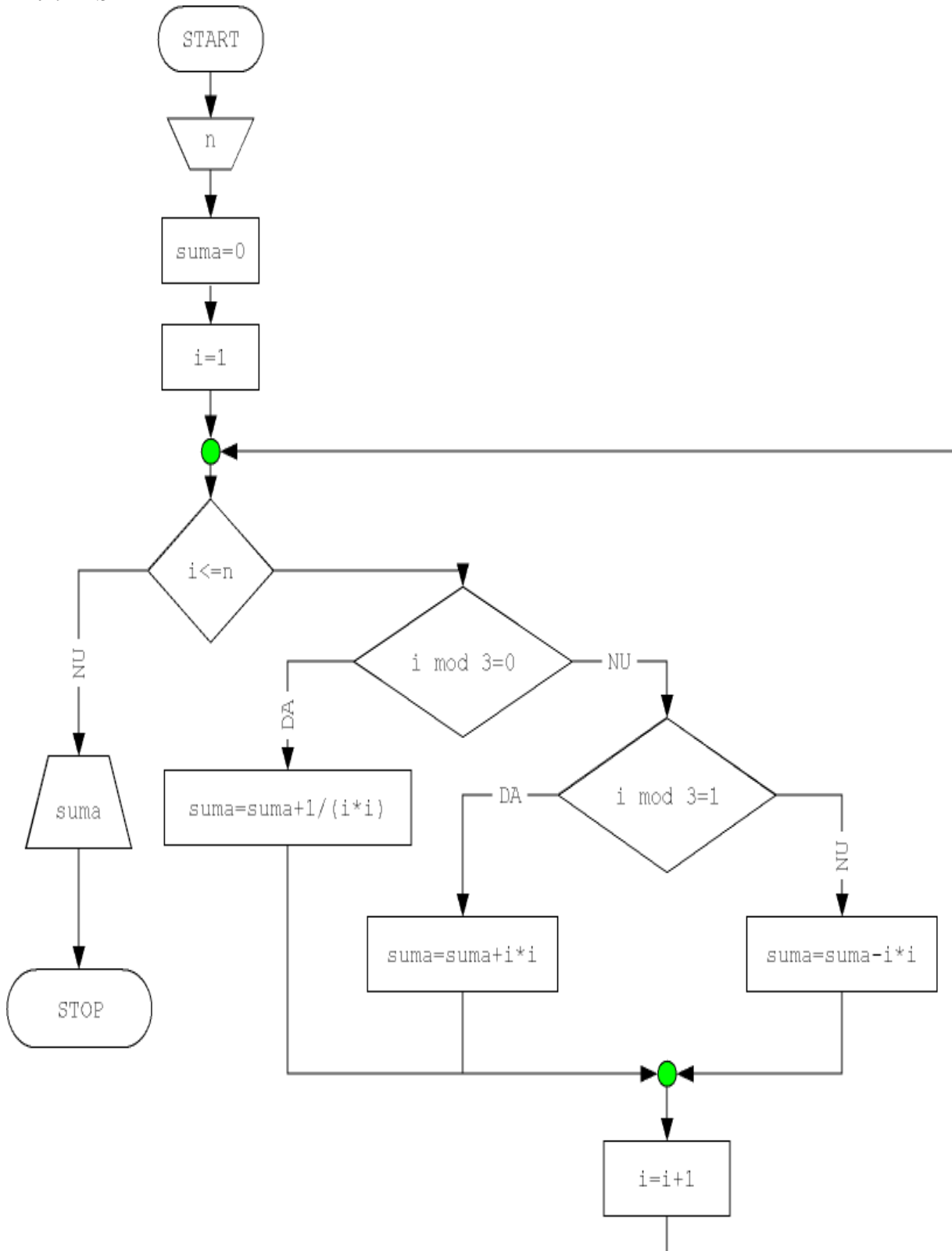
A2.19 – a



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A2.19 – b



Despre date si algoritmi

A2.20

